Specifications

Data Rate
- 1bps to 100 Mbps

Operating Voltage
- 4.5 to 5.5 V DC

Power Consumption
- 40 mW/MHz (nominal)

Data Interface
- I/O: TTL, CMOS

Modes
- A and B

Processing
- DESC-qualified (QMLV)
- CMOS Gate Array

Testing
- MIL-M-38510/
  MIL-STD-883 Level

Additional Testing
- Group E and DPA Available

Technology
- 1.2-micron Triple Level
  Metal Bulk CMOS Gate Array

MTTF
- >10M hours per
  MIL-HDBK-217E

Package
- 132-Pin Ceramic Flat Pack

Other
- Available in Class B

Features

- Endorsed by NSA for Type 1 applications
- Cryptographically compatible with the KG-227 and KG-228 flight units, KI-17, and Pegasus Ground Units (MYK-17B and KIV-7MS)
- Remote electronic keying (up to 64 keys can be indexed)
- Binary NRZ-L TTL format for PT and CT data, NRZ-M for CT data
- Maximum data rate of 100 Mbps.
- DESC-qualified JAN CMOS gate array manufacturer
- Endorsed by NSA for Type 1 applications
- Radiation hardened for space applications; 1M Rad (Si) Total Dose (>10M functional)
- Single-Event Upset (SEU) <1 x 10-10 errors/cell-day